

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 01-166684

(43)Date of publication of application : 30.06.1989

(51)Int.Cl.

H04N 7/137

(21)Application number : 62-323800

(71)Applicant : NIPPON HOSO KYOKAI <NHK>

(22)Date of filing : 23.12.1987

(72)Inventor : TANAKA YUTAKA

NOJIRI YUJI

HIRABAYASHI HIROSHI

(54) MOVING VECTOR DETECTION CIRCUIT

(57)Abstract:

PURPOSE: To expand the detection range of a moving vector by adding plural frame differences, detecting a minimum value, calculating an offset in horizontal and vertical directions from the result and controlling a representative point setting section and a switch.

CONSTITUTION: An output of the representative point setting section 2 connected to a frame memory 1 and subject to offset control and a preceding frame are subtracted by a subtractor 3 and the difference is added (4) by number of representative points. A minimum value of the sum of frame differences is detected (5) to detect a minimum value V_x and V_y of the moving vector in horizontal and vertical directions and to calculate an offset of the moving vector in horizontal and vertical directions thereby controlling the representative point setting section 2 and switches 6, 9 via horizontal and vertical offset control sections 11, 12. Moreover, values from offset devices 7, 10 and the minimum values V_x , V_y are added (8) to form horizontal and vertical moving vectors. Thus, the detection enable range of the moving vector is expanded rapidly.



LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]